



YAO-3950

PATENT *CAU2815*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshihisa Nagano et al. : Art Unit: 2815
Serial No.: 09/103,873 : Examiner: G. Eckert II
Filed: June 24, 1998 : Attn: Box Non-Fee
FOR: SEMICONDUCTOR DEVICE AND : Amendment
METHOD FOR FABRICATING THE SAME

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Bita
10/1/98*

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents and Trademarks
Washington, DC 20231

S I R :

This is in response to the Restriction Requirement stated in the
Office Letter dated September 9, 1998.

The Examiner requires that the claims of either Group I or Group
II be elected for prosecution. Applicant elects to prosecute Group I (claims
1-10, drawn to a semiconductor device). This election of claims is made without
traverse.

Respectfully submitted,

Andrew L. Ney

Andrew L Ney, Reg. No. 20,300
Attorney for Applicants

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Dated: September 25, 1998
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(610) 407-0700

The Assistant Commissioner for Patents is
hereby authorized to charge payment to
Deposit Account No. 18-0350 of any fees
associated with this communication.

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail,
with sufficient postage, in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C.
20231 on:

September 29, 1998

Andrew L. Ney
